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INFORMATION DISCLOSURE
STATEMENT
(Use several sheets if necessary)Att. Docket No.: 121744-0003 (CIT 3190-1)
B-75076S/N: 18028321
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Applicant: Hajimiri, et al.

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Filing Date: 12/19/2001

Group: 2681

U.S. PATENT DOCUMENTS

Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date if Appropriate
AA						

FOREIGN PATENT DOCUMENTS

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						Yes No
AB						

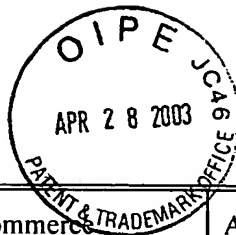
Other Documents (Title, Author, Date, Pages, Etc., if known)

UC	AC	Crois, Jan. et al., <i>A Single-Chip 900 MHz CMOS Receiver Front-End with a High Performance Low-IF Topology</i> , <u>IEEE Journal of Solid-State Circuits</u> , Vol. 30, No. 12, 1483-1492 (December 1995).
UC	AD	Devlin, Liam <i>Mixers</i> , Plectex Communications Technology Consultants, London Road, Great Chesterford, Essex.
UC	AE	Hashemi, et al., <i>Concurrent Dual-Band CMOS Low Noise Amplifiers and Receiver Architectures</i> , Department of Electrical Engineering, California Institute of Technology, Pasadena, CA, USA.
UC	AF	Henderson, Bert C., et al., <i>Image-Reject and Single-Sideband Mixers</i> , <u>WJ Tech Notes</u> , <u>Watkins-Johnson Company</u> Vol. 12 No. 3, (May-June 1985, revised and reprinted 2001).
UC	AG	Montemayor, Raymond, et al., <i>A Self-Calibrating 900 MHz CMOS Image-Reject Receiver</i> , Electrical Engineering Department, University of California, Los Angeles.
UC	AH	Rudell, Jacques C., et al., <i>Recent Developments in High Integration Multi-Standard CMOS Transceivers for Personal Communications Systems</i> , Department of Electrical Engineering and Computer Sciences, University of California at Berkeley
UC	AI	Weaver, Jr., Donald K., <i>A Third Method of Generation and Detection of Single-Sideband Signals</i> , <u>Proceedings of the IRE</u> , 1703-1705, (December 1956)
UC	AJ	Wu, Stephen, et al., <i>A 900-MHz/1.8-GHz CMOS Receiver for Dual-Band Applications</i> , <u>1998 IEEE International Solid-State Circuits Conference</u> , Session 8, Wireless Receivers (Paper FA 8.2).
UC	AK	Wu, Stephen, et al., <i>A 900-MHz/1.8-GHz CMOS Receiver for Dual-Band Applications</i> , <u>1998 IEEE Journal of Solid-State Circuits</u> , Vol. 33, No. 12, 2178-2185, (December 1998).

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Applicant: Hajimiri et al.

Filing Date: December 19, 2001 Group Art Unit: 2681

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Initial		Document Number								Date	Name	Class	Sub-Class	Filing Date if Appropriate
UC	AA	6	3	9	2	4	9	1		5/21/02	Ohkawa et al.	330	306	12/29/99
UC	AB	6	3	8	4	6	8	8		5/7/02	Fujioka et al.	330	302	10/25/00
UC	AC	6	3	2	9	8	8	6		12/11/01	Ogoro	333	32	5/11/99
UC	AD	6	0	7	5	9	9	6		6/13/00	Srinivas	455	552	11/25/97
UC	AE	5	5	9	2	1	2	2		1/7/97	Masahiro et al.	330	286	5/8/95
UC	AF	6	0	5	4	9	0	2		4/25/00	Masato	330	306	8/11/97
UC	AG	5	9	9	5	8	1	4		11/30/99	Yeh	455	180.1	6/13/97
UC	AH	5	9	7	3	5	6	8		10/26/99	Shapiro et al.	330	295	6/1/98

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							Yes	No
UC	AI	JP 05037255 (Abstract Only)	2/12/93	Japan	RECEIVED APR 29 2003 Technology Center 2600		X	
UC	AJ	DE 197 32 459 A1	2/4/99	Germany				X
UC	AK	WO 00/76060	12/14/00	WIPO			X	
UC	AL	DE 198 38 244 A1	2/24/00	Germany				X
UC	AM	EP 0 886 384 A2	12/23/98	Europe			X	

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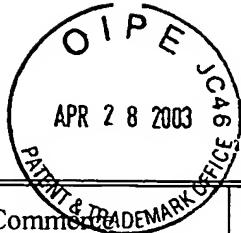
UC	AN	Search Report for PCT/US01/49805 dated September 19, 2002
UC	AO	Fong, K. <i>Dual-Band High-Linearity Variable-Gain Low-Noise Amplifiers for Wireless Applications</i> , <u>ISSCC Digest</u> , TP 13.3 (Feb. 1999)
UC	AP	Liu et al., <i>Dual-Frequency Planar Inverted-F Antenna</i> , <u>IEEE Trans. Ant. Prop.</u> , vol. 45, no. 10, pp 1451-58 (Oct. 1997)
UC	AQ	Miyake et al., <i>A Miniaturized Monolithic Dual Band Filter Using Ceramic Lamination Technique for Dual Mode Portable Telephones</i> , <u>IEEE MTT-S</u> , pp 789-92 (1997)

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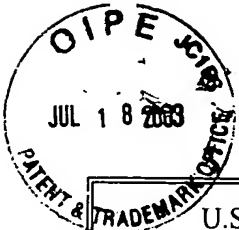
U	AR	Pozar et al., <i>A Dual-Band Circularly Polarized Aperture-Coupled Stacked Microstrip Antenna for Globe Positioning Satellite</i> , <u>IEEE Trans. Ant. Prop.</u> , vol. 45, no. 11, pp 1618-25 (Nov. 1997)
U	AS	Razavi, B., <i>A 900-MHz/1.8-GHz CMOS Transmitter for Dual-Band Applications</i> , <u>IEEE Journal of Solid-State Circuits</u> , vol. 35, no. 5 (May 1999)
U	AT	Ryynänen et al., <i>A Dual-Band RF Front-End for WCDMA and GSM Applications</i> , <u>CICC digest of Technical Papers</u> , pp 175-8 (May 2000)
U	AU	Samavati et al., <i>A 5-GHz CMOS Wireless LAN Receiver Front End</i> , <u>IEEE JSSC</u> , vol. 35, no. 5, pp 765-72 (May 2000)
U	AV	Shaeffer et al., <i>A 1.5-V, 1.5-GHz CMOS Low Noise Amplifier</i> , <u>IEEE JSSC</u> , vol. 32, no. 5, pp 745-59 (May 1997)
U	AW	Wu et al., <i>FA 8.2: A 900 MHz/1.8 GHz CMOS Receiver for Dual Band Applications</i> , <u>ISSCC Digest</u> , pp 8.2-8.208 (February 1998)

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Applicant: Hajimiri, et al.

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UC	AD	Hashemi, et al., <i>Concurrent Dual-Band CMOS Low Noise Amplifiers and Receiver Architectures</i> , Department of Electrical Engineering, California Institute of Technology, Pasadena, CA, USA.
UC	AE	Montemayor, Raymond, et al., <i>A Self-Calibrating 900 MHz CMOS Image-Reject Receiver</i> , Electrical Engineering Department, University of California, Los Angeles.
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